

## Refine Search

### Search Results -

Term	Documents
FPGA\$1	0
FPGA	12779
FPGAA	3
FPGAC	2
FPGAI	1
FPGAJ	11
FPGAL	1
FPGA0	2
FPGAS	5442
FPGAX	1
FPGAY	2
(L28 AND FPGA\$1 ).PGPB,USPT.	3

There are more results than shown above. [Click here to view the entire set.](#)

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L29

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Monday, January 31, 2005 [Printable Copy](#) [Create Case](#)

Set  
Name Query  
 side by  
 side

Hit  
Count Set  
Name  
 result set

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L29</u>	L28 and fpga\$1	3	<u>L29</u>
<u>L28</u>	L27 and l21	4	<u>L28</u>
<u>L27</u>	(714/725-741 )![CCLS]	3935	<u>L27</u>
<u>L26</u>	L25 and l21	28	<u>L26</u>
<u>L25</u>	(716/2-21 )![CCLS]	7281	<u>L25</u>
<u>L24</u>	L23 and l21	76	<u>L24</u>
<u>L23</u>	(712/2-300)[CCLS]	10663	<u>L23</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L22</u>	L21 and l17	135	<u>L22</u>
<u>L21</u>	L19 and (wireless or cellular or mobil\$6) and (network\$3 or lan or wan)	5288	<u>L21</u>
<u>L20</u>	L19 and l17	273	<u>L20</u>
<u>L19</u>	L18 and (dsp or digital near1 signal)	13665	<u>L19</u>
<u>L18</u>	(fpga\$1 or pla\$1 or pld\$1 or field near1 programmable) and real near1 time	51252	<u>L18</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L17</u>	(701/2-302)[CCLS]	25415	<u>L17</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L16</u>	L15 and secure	2	<u>L16</u>
<u>L15</u>	(fpga\$1 ) and l14	2	<u>L15</u>
<u>L14</u>	master and slave\$1 and L12	2	<u>L14</u>
<u>L13</u>	master and slaveL12	0	<u>L13</u>
<u>L12</u>	real near1 time near7 ecu	70	<u>L12</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L11</u>	(sleep or wak\$4 near1 up or fault\$4) and l10	1	<u>L11</u>
<u>L10</u>	(wireless or cellular or mobil\$5) and l6	1	<u>L10</u>
<u>L9</u>	(wireless or cellular or mobil\$5) and l8	0	<u>L9</u>
<u>L8</u>	6449273.pn. and clock\$3	1	<u>L8</u>
<u>L7</u>	6449273.pn. and synchronounous\$5	0	<u>L7</u>
<u>L6</u>	6275499.pn. and (synchronous\$5 or ashynchronous\$5)	1	<u>L6</u>
<u>L5</u>	6449273.pn. and (synchronous\$5 or ashynchronous\$5)	0	<u>L5</u>
<u>L4</u>	6449273.pn. and (synchronous\$5 or ashynchronous\$5)	0	<u>L4</u>
<u>L3</u>	6449273.pn. and macro\$8	0	<u>L3</u>
<u>L2</u>	6275499.pn. and pcmia	1	<u>L2</u>
<u>L1</u>	6275499.pn. and macro\$7	0	<u>L1</u>

END OF SEARCH HISTORY